

Procedure MEI/MEE-ESS

Structure	Possible time table structures, depending on student's choice: (i) Weekly 90min theory + 90 min practical training in lab. S081 (ii) 2-week rhythm: 180min lab and 180min classes. Classes: theory & simulation via Zoom and (a) CIP pool or (b) class room with own tablet PC
Location	A) Class/CIP for theory & simulation and B) practical in lab. S081
Application	Practical training: group building on circulating list
Preparation	Documentation from internet homepage + eLearning platform
Lab. Regulations	Observe safety regulations! Material for trainings: please use inteded cabinet drawer only. Ask supervisor if something is missing.
Instructions	Homepage of Prof. Schubert, online available: https://hps.hs-regensburg.de/~scm39115/homepage/education/courses/ms_ess/ms_ess.htm https://hps.hs-regensburg.de/~scm39115/homepage/education/labs/Lab_ElectronicBoards/Lab_ElectronicBoards.htm
Practical training	Labs and project to be edited in groups <u>New:</u> Embedded Hard Processor System (HPS) within Intel Cyclon V FPGA. How much embedded should we teach?: - Yes: <i>USB connection PC <=> HPS using embedded Linux:</i> Data: <i>HPS ⇔ VHDL via PuTTY</i> - No: <i>Ethernet connection PC <=> HPS (own Laptop required):</i> Exchange data <i>PC ⇔ microSD card via WinSCP</i> - No:: Prepare a <i>microSD Card</i> (on <i>MS Windows 10</i>) for embedded <i>Linux OS</i> using <i>Rufus</i> on Windows - No: - Learn <i>AXI (Advanced eXtensible Interface)</i> C ⇔ VHDL - Write&compile <i>C apps</i> for <i>ARM</i> processor on <i>Linux OS</i>.
Rating	Practical: Y/N test: operation of hard- and software (required) project work, marks weight: (project-lessons) / 28 written exam, 90min, weight: (28 – proj-lessons) / 28
Supervisor	Prof. M. Schubert
Rewards	Indicating 10 mistakes in the documentation → 1 bar of chocolate