

JTAG connector

Open to measure current
jumper JP3

External power connector
CON12

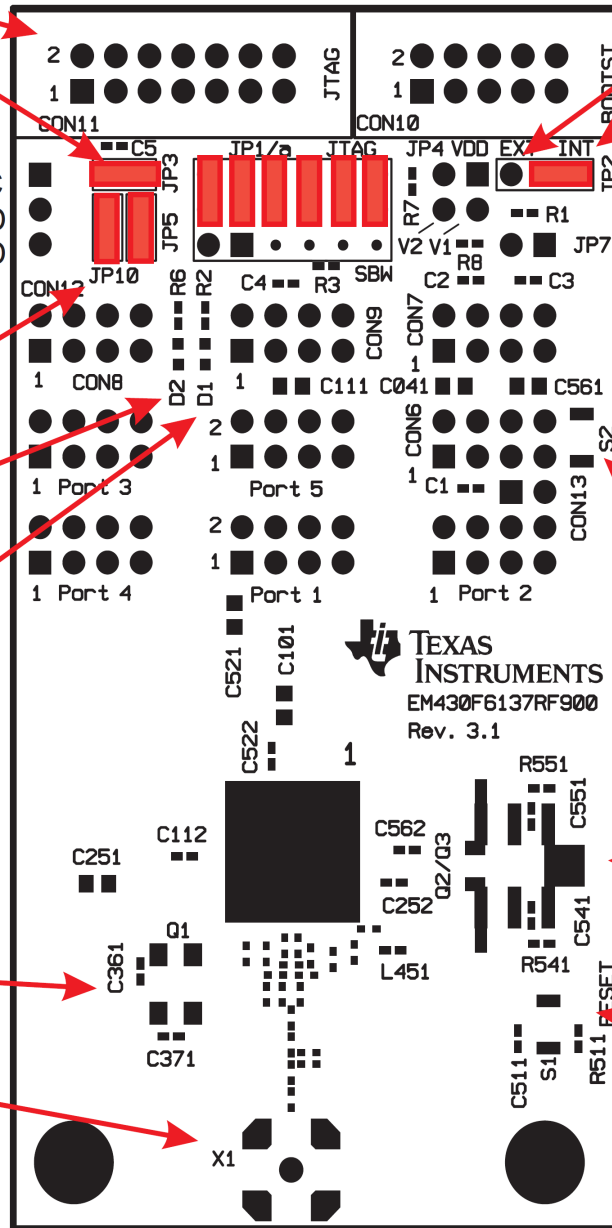
Open to disconnect LEDs
jumper JP5/JP10

LED D2 (red) connected to
P3.6 via JP10

LED D1 (green) connected
to P1.0 via JP5

RF - Crystal Q1 26 MHz

RF - Signal SMA



Jumper JP2
Close EXT for external supply
Close INT for JTAG supply

Jumper JP1
Close JTAG
position to debug in
JTAG mode

Jumper JP1
Spy-Bi-Wire mode



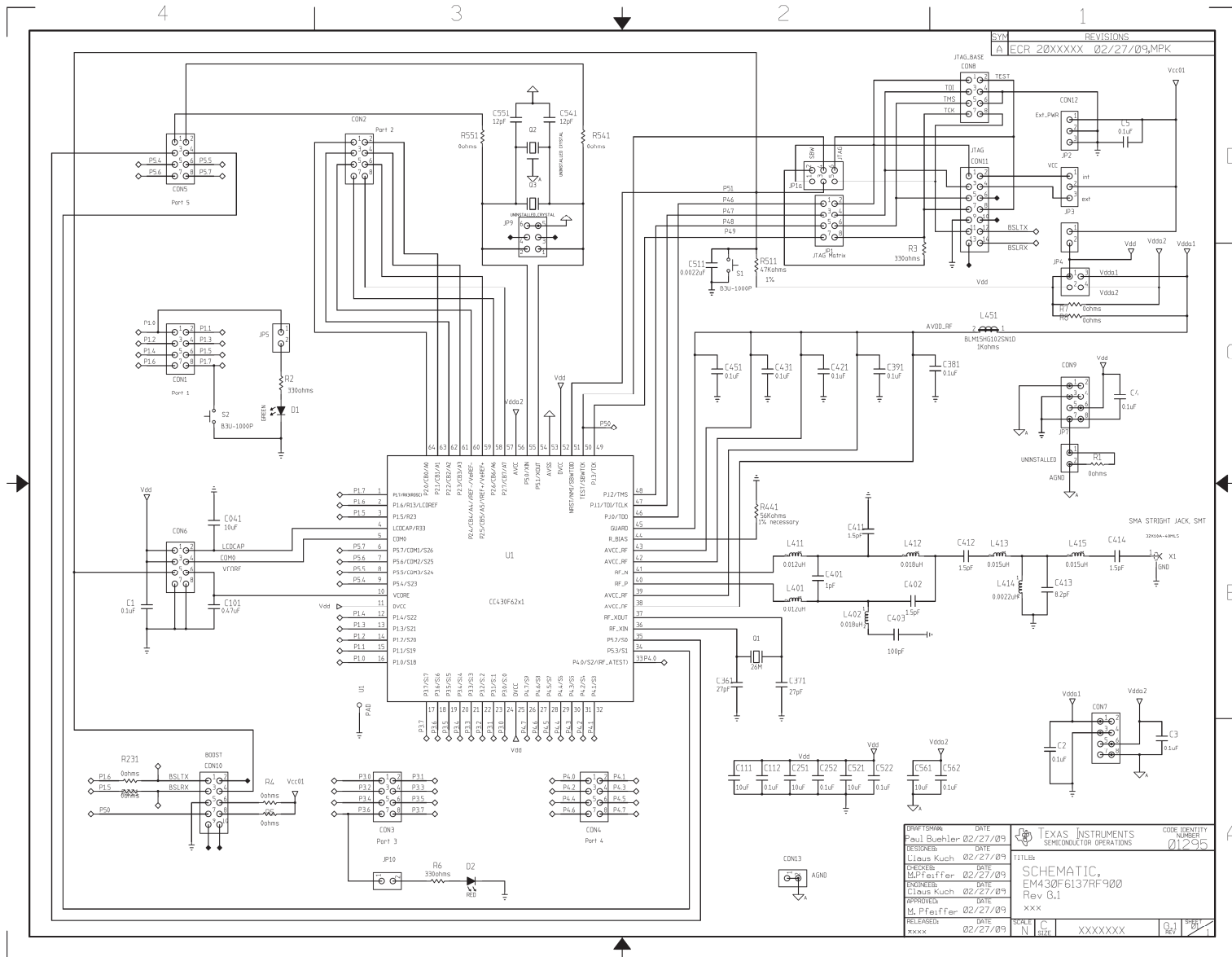
Close SBW position
to debug in
Spy-Bi-Wire mode

Push-button S2
connected to P1.7

Footprint for 32kHz crystal

Use 0Ω resistor for R541/R551
to make P5.0/P5.1 available
on connector port5

Reset button S1



Power Management

VCC01 = external VCC

Vdd = DVCC

Vdda1 = AVDD_RF / AVCC_RI

Vdda2 = AVCC

Port connectors

CON1 ..

CON5 = Port1 .. Port5 of cc430

CON6 = Vdd, GND, Vcore, COM0, LCDAP

CON7 = Vdda1, Vdda2, GND, AGND

CON8 = JTAG_BASE (JTAG Port)

CON9 = Vdd, GND, AGND

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Liou Kuch	02/27/09	Liou Kuch	02/27/09	Liou Kuch	02/27/09
RELEASED	DATE	RELEASED	DATE	RELEASED	DATE
xxxx	02/27/09	xxxx	02/27/09	xxxx	02/27/09