

## MEM-AKE: Ausgewählte Kapitel der Elektrotechnik, WiSe 2022/23, estimation

Timetable "Electr. Circ. & Systems" Theory			Diff.			
<b>Mo</b>	<b>Vorlesung und Ibor ESS</b>		days	<b>Mo</b>		
10.10	1	<b>C1: Introduction, Safety briefing, procedure</b> P1: Tools: Starting Matlab/Simulink, Quartus, Eagle	7	10.10	1	P1: Tools: Starting Matlab/Simulink, Quartus, Eagle
17.10	2	<b>C2: Switch-Mode Conversion</b>	7	17.10	2	<b>P2: Getting Started with DE1-SoC Board</b>
24.10	3	<b>C2: Switch-Mode Conversion</b>	7	24.10	3	<b>P3: @home: read p.1-6. Lab: p.7-11: heat, osci. PWM</b>
31.10	4	C2: ESS Switch Mode Conv. Bis p2.2.23 am 26.10.	7	31.10	4	<free>
7.11	5	<b>C2: Fertig</b> * C3:Matlab-Start * Debugging DCDCbuck	7	7.11	5	<b>Get.Start with DCDCbuck10 fertig, Debugging DCDCbu</b>
14.11	6	<b>C3: Simulink Intro., <del>ADA Conversion Modeling &amp; L4: Contro</del></b>	7	14.11	6	<b>P3: HPS, 3. Debugging fertig</b>
21.11	7	<b>C3: ADA Conversion Modeling: PWM-DAC + ADC\LTC2308</b>	7	21.11	7	<b>P4: Characterize Kap. 2.end, until p.8</b>
28.11	8	<b>C4: Simulink: QTF+Callback, Control Loops until p. 6</b>	7	28.11	8	<b>P4: Characterize Kap. 2.end, until p.12</b>
5.12	9	<b>C4: Control Loops finished incl. Fuzzy (p. 20)</b>	7	5.12	9	<b>P4: Characterize Kap. 3.2.end, until p.19</b>
12.12	10	<b>C5: Analog PID Controllers</b>	7	12.12	10	<b>P4: Char.: 3.end, pp. 20-28</b>
19.12	11	<b>C5: Analog PID Controllers</b>	7	19.12	11	<b>P4: Characterize, incl. ADC, Ch.4.3, pp.41-42</b>
9.1	12	<b>C6: Digitalization of analog PID Controllers</b>	21	9.1	12	<b>P4: Characterize: LG @ open loop, Ch.4.4, pp.43-47</b>
16.1	13	<b>C6: Digitalization of analog PID Controllers</b>	7	16.1	13	<b>P4: Characterize: LG @ closed loop, Ch.4.5, pp.48-53</b>
23.1	Semester ends		7	23.1	Semester ends	